

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Stephen W. Bedell, et al.

Examiner: Unassigned

Serial No: 10/055,138

Art Unit: Unassigned

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Docket: YOR920010499US1 (14710)

For: METHOD OF CREATING
HIGH-QUALITY RELAXED
SiGe-ON-INSULATOR FOR STRAINED
Si CMOS APPLICATIONS

Dated: March 28, 2003

Assistant Commissioner for Patents
United States Patent and Trademark Office
Washington, D.C. 20231

STATEMENT OF INVENTORSHIP UNDER 37 C.F.R. §1.48(a)(2)

Sir:

I, John Albrecht Ott, declare and say that:

(1) I am a co-inventor of the above-identified application;

(2) However, I was advised by counsel for assignee that I was not originally named as a co-inventor, but that the assignee is in the process of correcting the inventorship in the above-identified application;

(3) This error in inventorship in the above-identified application occurred without deceptive intention on my part.

(4) I declare further that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both under Section 1001 of Title 18 of the

United States Code and that such willful false statements may jeopardize the validity of any application or any patent issued thereon.

Dated: 4-2-2003


John Albrecht Ott